

The circuit diagram, labeled 15, shows a multi-stage CMOS logic circuit. It features two main power rails, V_{CC} at the top and ground at the bottom. The circuit includes several resistors ($R1, R2, R3, R4, R5, R6, R7$), capacitors ($C1, C2, C3$), and eleven transistors ($Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11$). The input signals are V_p and V_n , and the output is $V_{THRESH.}$. The circuit is configured with multiple stages of transistors and resistors, likely for signal conditioning or threshold detection. A reference voltage $V_{THRESH.}$ is also indicated.